

IEC TS

63107

Integration of arcing fault mitigation devices into power switchgear and control gear assemblies according to IEC61439-2



16th April 2020 09.45am – 11am (CEST)

LV switchgear assemblies are undoubtedly the components of the electric installation more subject to the direct intervention of personnel (operations, maintenance, etc.).

In case of wrong action or external fault caused i.e. by animals, an Arc faults may happen and result in catastrophic damages to both switchgear assemblies and humans.

Besides, there may be operational loss to critical production processes, which at breakdown are forced to downtime. Within a few milliseconds, high amounts of energy are released, generating heat, a pressure wave and toxic gases like an explosion.

Such damages might be reduced using arcing fault mitigation devices with quenching time of less milliseconds.

The IECTS63107 states requirements for integration and testing of internal arc-fault mitigation systems in low-voltage switchgear and control gear assemblies – PSC-assemblies according to IEC 61439-2 to demonstrate their correct operation.

The webinar presents the coming brand-new Technical Specification, with respect to its application, and the related test services offered from KEMA Labs with reference to.

AGENDA

- Webinar and KEMA Labs Introduction
- Integration in the International standard system
- Scope of TS63107
- Components and basic structure
- Protected and non-protected areas
- Design Verification
- Q&A

SPEAKER

Cristian La Salvia- Market and Product development manager Low Voltage KEMA Labs

Ronald Borchert – Senior Test Engineer Low Voltage KEMA Labs

KEMA Labs